

**MOSER
PATTERSON &
SHERIDAN, LLP**

ATTORNEYS AT LAW

3040 Post Oak Blvd, Suite 1500
Houston, TX 77056-6582
TEL 713.623.4844
FAX 713.623.4846
WWW.MPSLLP.COM

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DATE: July 19, 2004
FILE NO: INFN/0026 (2003P52879US)
TO: Examiner Linh Nguyen
FAX NO: 703.872.9306
PHONE NO:
COMPANY: USPTO
FROM: Gero G. McClellan
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ORIGINAL TO FOLLOW? ☐ YES ☐ NO

RESPONSE TO OFFICE ACTION DATED APRIL 19, 2004

TITLE: DELAYED LOCKED LOOP PHASE BLENDER CIRCUIT
U.S. SERIAL NO.: 10/696,920
FILING DATE: October 30, 2003
INVENTOR: Kim et al.
EXAMINER: Linh Nguyen
GROUP ART UNIT: 2816
CONFIRMATION NO.: 8247

CONFIDENTIALITY NOTE

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PATENT
Infineon Dkt. No.: 2003P52879US
OC Docket No.: INFN/0026

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JUL 19 2004

In re Application of:
Kim et al.

Serial No.: 10/696,920

Confirmation No.: 8247

Filed: October 30, 2003

For: Delayed Locked Loop Phase
Blender Circuit

Group Art Unit: 2816

Examiner: Nguyen, L.

OFFICIAL

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF FAX 37 CFR 1.8	
I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office to fax number 703/872-9306 to the attention of Examiner Linh Nguyen on the date below:	
<u>7-19-2004</u> Date	<u>[Signature]</u> Gero G. McClellan

Dear Sir:

RESPONSE TO OFFICE ACTION DATED APRIL 19, 2004

In response to the Office Action dated April 19, 2004, having a shortened statutory period for response set to expire on July 19, 2004, please enter this response and reconsider the claims pending in the application for reasons discussed below. While no fees are believed due, the Commissioner is hereby authorized to charge counsel's Deposit Account No. 20-0782/INFN/0026/GGM for any fees, including extension of time fees or excess claim fees, required to make this response timely and acceptable to the Office.

A listing of claims begins on page 2 of this paper. Remarks begin on page 7 of this paper.

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